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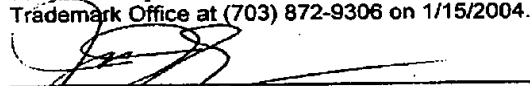
Appl. No.:	09/938,260	Confirmation No.	1259
Applicant:	Steven W. Russell	Examiner:	Jesse A. Fenty
Filed:	8/23/2001	TC/A.U.:	2815
Docket No.:	TI-25084	Customer No.:	23494
Title:	IMPROVED DIELECTRIC LAYER LINER FOR AN INTEGRATED CIRCUIT STRUCTURE		

AMENDMENT UNDER 37 CFR 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at (703) 872-9306 on 1/15/2004.


Jacqueline J. Garner
Reg. No. 36,144

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated 9/15/2003. They are respectfully submitted as a full and complete response to that Action.

Please amend the above-referenced application as follows:

There are no **Amendments to the Specification**.

Amendments to the Claims are reflected in the listing of claims that begin on page 2 of this paper.

There are no **Amendments to the Drawings**.

The **Remarks** begin on page 4.

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Total Pages Sent 9

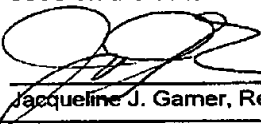
From: Jacqueline J. Garner
Texas Instruments Incorporated
Facsimile: 972-917-4418
Phone: 214-532-9348

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Jacqueline J. Garner, Reg. No. 36,144

January 15, 2004
Date**FACSIMILE COVER SHEET**

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<input type="checkbox"/> CONTINUATION APP'N (# Pages)	
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Russell	
TITLE OF INVENTION: Improved Dielectric Layer Liner . . .	
TI FILE NO.: TI-25084	DEPOSIT ACCT. NO.: 20-0668
FAXED: 1/15/2004 DUE: 12/15/2003 ATTY/SEC'Y: JJG	
RECEIPT DATE & SERIAL NO.: Serial No.: 09/938,260 Filing Date: 8/23/2001 Conf. No.: 1259	

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